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It is submitted that the Information Disclosure Statement is in compliance with 37 CFR 1.98 and the Examiner is respectfully requested to consider the listed references.

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Dated: December 5, 2005

Respectfully submitted,

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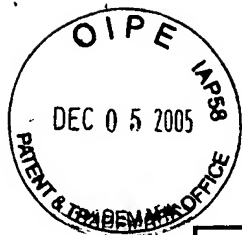
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Substitute for form 1449A/B/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)			Complete if Known		
			Application Number	10/695,921-Conf. #7033	
			Filing Date	October 28, 2003	
			First Named Inventor	Ols Hidri	
			Art Unit	2838	
			Examiner Name	A. D. Berhane	
Sheet	1	of	1	Attorney Docket Number	08211/0200347-US0/P05786

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			

FOREIGN PATENT DOCUMENTS						
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	CA	BUCK et al., "A CMOS Bandgap Reference Without Resistors", IEEE Journal of Solid-State Circuits, Vol. 37, No. 1, pp. 81-83, January 2002.	

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Examiner Signature		Date Considered	
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